

Docket No.: M4065.0322/P322-A

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Eric T. Stubbs et al.

Group Art Unit: 2186

Application No.: 10/620,612

Examiner: Tuan V. Thai

Filed: July 17, 2003

Allowed: October 17, 2006

For: INTEGRATED SEMICONDUCTOR MEMORY CHIP WITH PRESENCE

DETECT DATA CAPABILITY

## COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants agree in part with the Examiner's statement of reasons for allowance in that the prior art of record fails to disclose, teach or suggest the claimed invention. However, Applicants note that the Examiner's statement fails to address all limitations of all of the allowed claims. For example, certain claim limitations were not discussed in the Examiner's Reasons for Allowance, including those in the dependent claims, each of which defines a unique combination of features not shown or suggested by the prior art, providing additional reasons for allowance of each claim.

Application No.: 10/620,612 Docket No.: M4065.0322/P322-A

Furthermore, Applicants respectfully submit that the scope of the claimed invention should not be limited by the Examiner's statement, but rather the scope of the claimed invention should be based on the actual language of the allowed claims.

Dated: November 6, 2006

Respectfully submitted,

Thomas J. D'Amico

Registration No. 28,371

Gianni Minutoli

Registration No. 41,198

DICKSTEIN SHAPIRO LLP

1825 Eye Street, NW

Washington, DC 20006

(202) 420-2232

Attorneys for Applicants